



TEST REPORT

EN IEC 62680-1-2:2022

EN IEC 62680-1-3:2022

Product : Tablet

Trade Mark : CUBOT

Model Name : TAB KINGKONG MINI

Family Model : N/A

Report No. : S25052400913001

Prepared for

Shenzhen Huafurui Technology Co., Ltd.

Unit 601-03, 6/F, Block A, Building 1, Ganfeng Technology Building, No. 993 Jiaxian Road, Xiangjiaotang Community, Bantian Street, Longgang District, Shenzhen, P.R. China

Prepared by

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TEST RESULT CERTIFICATION

Applicant's name Shenzhen Huafurui Technology Co., Ltd.
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Manufacturer's Name Shenzhen Huafurui Technology Co., Ltd.
Address..... Unit 601-03, 6/F, Block A, Building 1, Ganfeng Technology Building, No. 993 Jiaxian Road, Xiangjiaotang Community, Bantian Street, Longgang District, Shenzhen, P.R. China

Product description

Product name Tablet
Trademark CUBOT
Model Name TAB KINGKONG MINI
Family Model N/A

Standards EN IEC 62680-1-2:2022
..... EN IEC 62680-1-3:2022

This device described above has been tested by Shenzhen NTEK, and the test results show that the equipment under test (EUT) is in compliance with the 2014/53/EU RED Directive Art.3.3a requirements. And it is applicable only to the tested sample identified in the report.

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Test Sample Number..... : S250524009001

Date of Test.....:

Date (s) of performance of tests: Jul. 18, 2025

Date of Issue.....: Jul. 18, 2025

Test Result.....: **Pass**

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(Project Engineer)

Reviewed By : Aaron Cheng
Aaron Cheng
(Supervisor)

Approved By : Alex Li
Alex Li
(Manager)

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Revision History

Report No.	Version	Description	Issued Date
S25052400913001	Rev.01	Initial issue of report	Jul. 18, 2025

1. GENERAL INFORMATION

1.1 GENERAL DESCRIPTION OF EUT

Equipment	Tablet
Trade Mark	CUBOT
Model Name.	TAB KINGKONG MINI
Family Model	N/A
Model Difference	N/A
Rating	DC 3.87V from Battery or DC 5V/9V/12V from Adapter 1 or DC 5V/9V/12V/15V/20V from Adapter 2
EUT Function Description	Please reference user manual of this device
Hardware Version	T30D-UF-V1.2
Software Version	CUBOT_TAB_KINGKONG_MINI_P131_V1.0
Firmware Version	N/A

Critical components Information:

/	Manufacturer	Model	Technical data	Standard
PD Charging IC	SouthChip	NX8530CFFR	/	EN IEC 62680-1-2:2022 Test with equipment
Type-C receptacle connector	Dongguan City Sanji Electronics Co., Ltd.	UC1-040EBQT-NAYD	Number of pins: 24	EN IEC 62680-1-3:2022 TID 11166

The Type-C receptacle is 24 pins as below figure:

<u>A1</u>	<u>A2</u>	<u>A3</u>	<u>A4</u>	<u>A5</u>	<u>A6</u>	<u>A7</u>	<u>A8</u>	<u>A9</u>	<u>A10</u>	<u>A11</u>	<u>A12</u>
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
<u>B12</u>	<u>B11</u>	<u>B10</u>	<u>B9</u>	<u>B8</u>	<u>B7</u>	<u>B6</u>	<u>B5</u>	<u>B4</u>	<u>B3</u>	<u>B2</u>	<u>B1</u>
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>

RoHS Complied

REVISIONS

Technical drawing of a panel design. The drawing shows a cross-section of a panel with a plug and a PCB. Dimensions are indicated in parentheses: (R1.05), (8.45), (2.65), (6.65), (3.5), (3.75), and (0.15). The text "REF. PANEL DESIGN" is present, along with a warning symbol (a triangle with an exclamation mark) and the label "PCB".

①	BOND	---	1PCS	---	---
②	GP2	---	1PCS	SUS301 T=0.1	Ni plating
③	GP1	---	1PCS	SUS301 T=0.1	Ni plating
④	LP	---	1PCS	4000 6000 ML	UL 94HB
⑤	U	---	1PCS	SUS304 T=0.2	UL
⑥	SHELL-1	---	1PCS	SUS304 T=0.15	Ni plating
⑦	SHELL-2	---	1PCS	SUS316L T=0.2	鍍十鍍銀
⑧	MIDDLE MEXAL	---	1PCS	SUS301 T=0.15	Ni plating
⑨	CONTACT	BI=BI12 AI=AI2	2SET	C7025 T=0.12	Under plating: Ni plating Contact area: Gold plating Soldering area: Gold plating
⑩	HOUSING	---	1PCS	PA9T(BLAK)	UL 94V-0
ITEM	PART NAME	TERMINAL	QTY	MATERIAL	REMARK
DSND	Esson	DATE	2024.05.07		NAME
DWN	Esson	DATE	2024.05.07	3RD ANGLE PROJECTION	USE TYPE-C 24PIN CONNECTOR
CHKD	Esson	DATE	2024.05.07		CAT. NO.
APVD	Esson	DATE	2024.05.07	UNIT: mm	UC1-06389QW-NDY6
司	SANJI ELECTRONICS CO., LTD				DWN.NO. A-UC1-06389QW-NDY6
1/1					1/1

UNLESS OTHERWISE SPECIFIED, TOLERANCE ON DECIMAL: ± 0.2

VIF:

Product Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID Optional Content	
<p>Certification_Type 0: End Product</p>	<p>Port Label 0</p> <p>Connector_Type 2: Type-C®</p> <p>USB4_Supported NO</p> <p>USB4_Router_Index</p> <p>USB_PD_Support YES</p> <p>PD_Port_Type 4: DRP</p> <p>Type_C_State_Machine 2: DRP</p> <p>Port_Battery_Powered YES</p> <p>BC_1_2_Support 1: Portable Device</p>
<p>PD_Spec_Revision_Major 3</p> <p>PD_Spec_Revision_Minor 1</p> <p>PD_Spec_Version_Major 1</p> <p>PD_Spec_Version_Minor 8</p> <p>PD_Specification_Revision 2: Revision 3</p> <p>Security_Mgmt_Supported_SQ NO</p> <p>Manufacturer_Info_Supported_Pi NO</p> <p>Manufacturer_Info_VID_Pid</p> <p>Manufacturer_Info_PID_Pid</p> <p>Num_Fixed_Battery 1</p> <p>Num_Swappable_Battery_Slot 0</p> <p>SOP*</p> <p>SOP_Capable YES</p> <p>SOP_P_Capable NO</p> <p>SOP_PP_Capable NO</p> <p>SOP_P_Debug_Capable NO</p> <p>SOP_PP_Debug_Capable NO</p> <p>ID_Header_Connector_Type_SQ 2: USB Type-C® Receptacle</p> <p>Unconstrained_Power NO</p> <p>Chunking_Implemented_SQ NO</p> <p>Unchunked_Extended_Messages_Support NO</p>	<p>USB_Comms_Capable YES</p> <p>Data_Reset_Supported NO</p> <p>DR_Swap_To_DFP_Support YES</p> <p>DR_Swap_To_UFP_Support YES</p> <p>VCONN_Swap_To_On_Support NO</p> <p>VCONN_Swap_To_Off_Support NO</p> <p>Responds_To_Discover_SOP_UH YES</p> <p>Responds_To_Discover_SOP_DR NO</p> <p>Attempts_Discover_SOP YES</p> <p>Power_Interruption_Available 0: No Interruption Possible</p>
<p>Type_C_Implements_Try_SR NO</p> <p>Type_C_Implements_Try_SN NO</p> <p>Rp_Value 0: Default</p> <p>Type_C_Supports_VCONN_Powered_Access <select></p> <p>Type_C_Is_VCONN_Powered_Access NO</p> <p>Type_C_Is_Debug_Target_SR NO</p> <p>Type_C_Is_Debug_Target_SN NO</p> <p>Type_C_Can_Act_As_Host YES</p> <p>Type_C_Can_Act_As_Device YES</p> <p>Type_C_Is_Alt_Mode_Controlled NO</p> <p>Type_C_Is_Alt_Mode_Adapted NO</p> <p>Type_C_Power_Source 2: Both</p> <p>Type_C_Port_On_Hub NO</p> <p>Type_C_Supports_Audio_Access YES</p> <p>Type_C_Sources_VCONN NO</p>	<p>Product_Total_Source_Power_mW 33000</p> <p>Port_Source_Power_Type 0: Assured</p> <p>Port_Source_Power_Gang</p> <p>Port_Source_Power_Gang_Max_Power</p>

Product: Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID

Optional Content

Host_Supports_USB_Dat YES

Host_Speed 0: USB 2

Host_Contains_Captive_Retrim NO

Host_Truncates_DP_For_SDPResponse <select>

Host_Gen1x1_tLinkTurnaround

Host_Gen2x1_tLinkTurnaround

Host_Is_Embedded NO

Host_Suspend_Support YES

Is_DFP_On_Hub NO

Hub_Port_Number

Product: Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID

Optional Content

Device_Supports_USB_Dat YES

Device_Speed 0: USB 2

Device_Max_USB2_Speed 2: High Speed

Device_Contains_Captive_Retrim NO

Device_Truncates_DP_For_SDPResponse <select>

Device_Gen1x1_tLinkTurnaround

Device_Gen2x1_tLinkTurnaround

Hub_Billboard_DSPort_Num Update

Product: Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID

Optional Content

PD_Power_As_Source 2000

EPR_Supported_As_Sr NO

Has_Invariant_PDOs YES

Port_Managed_Guaranteed_Type 0: Managed Capability

Sends_Finings NO

USB_Suspend_May_Be_Clear NO

Master_Port NO

FR_Swap_Type_Current_Capability_As_Initial_S 0: FR_Swap not supported

Over-Current Protection

PD_OC_Protection NO

PD_OC_Method <select>

Num_Src_PDOs 1

Source PDOs

Source PDO 1: Fixed Supply - voltage 5000 mV, max current 400 mA, peak current 100% IOC.

Add... Edit... Insert... Delete Clear

Product: Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID

Optional Content

PD_Power_As_Sink 10000

EPR_Supported_As_Sr NO

No_USB_Suspend_May_Be_S4 YES

GiveBack_May_Be_S4 NO

Higher_Capability_Sr NO

FR_Swap_Reqd_Type_Current_As_Initial_S 0: FR_Swap not supported

Num_Sink_PDOs 2

Sink PDOs

Sink PDO 1: Fixed Supply - voltage 5000 mV, operating current 2000 mA.

Sink PDO 2: SFR Augmented Supply - operating current 2000 mA, min voltage 3300 mV, max voltage 11000 mV.

Add... Edit... Insert... Delete Clear

Product: Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID

Optional Content

Accepts_FR_Swap_As_Sr YES

Accepts_FR_Swap_As_Sr YES

Requests_FR_Swap_As_Sr YES

Requests_FR_Swap_As_Sr YES

FR_Swap_Supported_As_Initial_S NO

Product: Component General PD PD Capabilities USB Type-C® Product Power USB Host USB Device PD Source PD Sink Dual Role SOP Discover ID

Optional Content

XID_SOP 0

Data_Capable_As_USB_Host_SQ YES

USB_VID_SOP 1782

Data_Capable_As_USB_Device_SQ YES

PID_SOP 6360

Product_Type_UFP_SOP 2: PDUSB Peripheral

bcdDevice_SOP 0000

Product_Type_DFP_SOP 2: PDUSB Host

DFP_VDO_Port_Number

Model_Operation_Supported_SQ NO

1.2 TEST FACILITY

Shenzhen NTEK Testing Technology Co., Ltd.

Address: No. 24 Xinfu East Road, Xiangshan Community, Xinqiao Street, Bao'an District, Shenzhen, Guangdong, China

FCC Registered No.: 463705 IC Registered No.:9270A-1

CNAS Registration No.:L5516

2. Compliance of EN IEC 62680-1-3:2022

2.1 Assess result

Clause	Requirement	Remark	Verdict
2	Overview		Pass
2.1	Introduction		Pass
2.2	USB Type-C Receptacles, Plugs and Cables	Approved 24-pin type-C receptacle used, see Critical components for details	Pass
2.3	Configuration Process	Type-C Functional Test Result as below	Pass
2.3.1	Source-to-Sink Attach/Detach Detection		Pass
2.3.2	Plug Orientation/Cable Twist Detection		Pass
2.3.3	Initial Power (Source-to-Sink) Detection and Establishing the Data (Host-to-Device) Relationship		Pass
2.3.4	USB Type-C VBUS Current Detection and Usage		Pass
2.3.5	USB PD Communication	Type-C Functional Test Result as below	Pass
2.3.6	Functional Extensions		N/A
2.4	VBUS	Default USB power level, 1.5A and 3A considered.	Pass
2.5	VCONN		N/A
2.6	Hubs		N/A
3	Mechanical		Pass
3.1	Overview		Pass
3.1.1	Compliant Connectors		Pass
3.1.2	Compliant Cable Assemblies		N/A
3.1.3	Compliant USB Type-C to Legacy Cable Assemblies		N/A
3.1.4	Compliant USB Type-C to Legacy Adapter Assemblies		N/A
3.2	USB Type-C Connector Mating Interfaces	Approved receptacle used, see Critical components for details	Pass
3.2.1	Interface Definition		Pass
3.2.2	Reference Designs		Pass
3.2.3	Pin Assignments and Descriptions		Pass
3.3	Cable Construction and Wire Assignments		N/A
3.3.1	Cable Construction (Informative)		N/A

3.3.2	Wire Assignments		N/A
3.3.3	Wire Gauges and Cable Diameters (Informative)		N/A
3.4	Standard USB Type-C Cable Assemblies		N/A
3.4.1	USB Full-Featured Type-C Cable Assembly		N/A
3.4.2	USB 2.0 Type-C Cable Assembly		N/A
3.4.3	USB Type-C Captive Cable Assemblies		N/A
3.4.4	USB Type-C Thumb Drive Assemblies		N/A
3.5	Legacy Cable Assemblies		N/A
3.5.1	USB Type-C to USB 3.1 Standard-A Cable Assembly		N/A

Clause	Requirement	Remark	Verdict
3.5.2	USB Type-C to USB 2.0 Standard-A Cable Assembly		N/A
3.5.3	USB Type-C to USB 3.1 Standard-B Cable Assembly		N/A
3.5.4	USB Type-C to USB 2.0 Standard-B Cable Assembly		N/A
3.5.5	USB Type-C to USB 2.0 Mini-B Cable Assembly		N/A
3.5.6	USB Type-C to USB 3.1 Micro-B Cable Assembly		N/A
3.5.7	USB Type-C to USB 2.0 Micro-B Cable Assembly		N/A
3.6	Legacy Adapter Assemblies		N/A
3.6.1	USB Type-C to USB 3.1 Standard-A Receptacle Adapter Assembly		N/A
3.6.2	USB Type-C to USB 2.0 Micro-B Receptacle Adapter Assembly		N/A
3.7	Electrical Characteristics		N/A
3.7.1	Raw Cable (Informative)		N/A
3.7.2	USB Type-C to Type-C Passive Cable Assemblies (Normative)		N/A
3.7.3	Mated Connector (Informative – USB 3.2 Gen2 and USB4 Gen2)		N/A
3.7.4	Receptacle Connector SI Requirements and Testing (Normative –USB4 Gen3)		N/A
3.7.5	USB Type-C to Legacy Cable Assemblies (Normative)		N/A
3.7.6	USB Type-C to USB Legacy Adapter Assemblies (Normative)		N/A
3.7.7	Shielding Effectiveness Requirements (Normative)		N/A
3.7.8	DC Electrical Requirements (Normative)	Approved connector used	N/A
3.8	Mechanical and Environmental Requirements	Approved receptacle used, see Critical components for details	N/A
3.8.1	Mechanical Requirements		N/A
3.8.2	Environmental Requirements		N/A
3.9	Docking Applications (Informative)		N/A

3.10	Implementation Notes and Design Guides	Approved receptacle used, see Critical components for details	N/A
3.10.1	EMC Management (Informative)		N/A
3.10.2	Stacked and Side-by-Side Connector Physical Spacing (Informative)		N/A
3.10.3	Cable Mating Considerations (Informative)		N/A
3.11	Extended Power Range (EPR) Cables		N/A
3.11.1	Electrical Requirements		N/A
3.11.2	EPR Cable Identification Requirements		N/A
4	Functional		Pass
4.1	Signal Summary		---
4.2	Signal Pin Descriptions		---
4.2.1	SuperSpeed USB Pins		N/A
4.2.2	USB 2.0 Pins		N/A

Clause	Requirement	Remark	Verdict
4.2.3	Auxiliary Signal Pins		N/A
4.2.4	Power and Ground Pins		Pass
4.2.5	Configuration Pins		Pass
4.3	Sideband Use (SBU)		N/A
4.4	Power and Ground		Pass
4.4.1	IR Drop		N/A
4.4.2	VBUS		Pass
4.4.3	VCONN		N/A
4.5	Configuration Channel (CC)	Type-C Functional Test as below	Pass
4.5.1	Architectural Overview		Pass
4.5.2	CC Functional and Behavioral Requirements		Pass
4.5.3	USB Port Interoperability Behavior		Pass
4.6	Power	Type-C Functional Test as below	Pass
4.6.1	Power Requirements during USB Suspend		Pass
4.6.2	VBUS Power Provided Over a USB Type-C Cable		Pass
4.7	USB Hubs		N/A
4.8	Power Sourcing and Charging	Sink only	Pass
4.8.1	DFP as a Power Source		Pass
4.8.2	Non-USB Charging Methods		Pass
4.8.3	Sinking Host		N/A
4.8.4	Sourcing Device		N/A
4.8.5	Charging a System with a Dead Battery		Pass
4.8.6	USB Type-C Multi-Port Chargers		N/A
4.9	Electronically Marked Cables		N/A

4.9.1	Parameter Values		N/A
4.9.2	Active Cables		N/A
4.10	VCONN-Powered Accessories (VPAs) and VCONN- Powered USB Devices (VPDs)		N/A
4.10.1	VCONN-Powered Accessories (VPAs)		N/A
4.10.2	VCONN-Powered USB Devices (VPDs)		N/A
4.11	Parameter Values		Pass
4.11.1	Termination Parameters		Pass
4.11.2	Timing Parameters		Pass
4.11.3	Voltage Parameters		Pass
5	USB4 Discovery and Entry		N/A
5.1	Overview of the Discovery and Entry Process	No USB4	N/A
5.2	USB4 Functional Requirements		N/A
5.2.1	USB4 Host Functional Requirements		N/A
5.2.2	USB4 Device Functional Requirements		N/A
5.2.3	USB4 Alternate Mode Support		N/A
5.2.3.1	USB4 Alternate Mode Support on Hosts		N/A
5.2.3.2	USB4 Alternate Mode Support on Hubs and USB4- based Docks		N/A
5.3	USB4 Power Requirements		N/A

Clause	Requirement	Remark	Verdict
5.3.1	Source Power Requirements		N/A
5.3.2	Sink Power Requirements		N/A
5.3.3	Device Power Management Requirements		N/A
5.4	USB4 Discovery and Entry Flow Requirements		N/A
5.4.1	USB Type-C Initial Connection		N/A
5.4.2	USB Power Delivery Contract		N/A
5.4.3	USB4 Discovery and Entry Flow		N/A
5.4.3.1	USB4 Device Discovery (SOP)		N/A
5.4.3.2	USB4 Cable Discovery (SOP')		N/A
5.4.3.3	USB4 Operational Entry		N/A
5.4.4	USB4 Post-Entry Operation		N/A
5.4.4.1	During USB4 Operation		N/A
5.4.4.2	Exiting USB4 Operation		N/A
5.5	USB4 Hub Connection Requirements		N/A
5.5.1	USB4 Hub Port Initial Connection Requirements		N/A
5.5.2	USB4 Hub UFP and Host Capabilities Discovery		N/A

5.5.3	Hub DFP Connection Requirements		N/A
5.5.3.1	Speculative Connections		N/A
5.5.3.2	Operational Connections		N/A
5.5.4	Hub Ports Connection Behavior Flow Examples		N/A
5.5.5	Connecting to Downstream USB4 Hubs		N/A
5.5.6	Fallback Functional Requirements for USB4 Hubs		N/A
5.6	USB4 Device Connection Requirements		N/A
5.6.1	Fallback Mapping of USB4 Peripheral Functions to USB Device Class Types		N/A
5.7	Parameter Values		N/A
5.7.1	Timing Parameters		N/A
6	Active Cables		N/A
6.1	USB Type-C State Machine	No active cable	N/A
6.2	USB PD Requirements		N/A
6.2.1	Active Cable USB PD Requirements		N/A
6.2.2	USB PD Messages for OIAC		N/A
6.2.3	Short Active Cable Behaviors in Response to Power Delivery Events		N/A
6.3	OIAC Connection Flow and State Diagrams		N/A
6.3.1	OIAC Connection Flow – Discovery – Phase 1		N/A
6.3.2	OIAC Connection Flow – Reboot – Phase 2		N/A
6.3.3	OIAC Connection Flow – Configuration – Phase 3		N/A
6.3.4	OIAC Connection State Diagram Plug-A		N/A

Clause	Requirement	Remark	Verdict
6.3.5	OIAC Connection State Diagram Plug-B		N/A
6.4	Active Cable Power Requirements		N/A
6.4.1	VBUS Requirements		N/A
6.4.2	OIAC VBUS Requirements		N/A
6.4.3	USB PD Rules in Active State		N/A
6.4.4	VCONN Requirements		N/A
6.5	Mechanical		N/A
6.5.1	Thermal		N/A
6.5.2	Plug Spacing		N/A
6.6	Electrical Requirements		N/A
6.6.1	Shielding Effectiveness Requirement		N/A
6.6.2	Low Speed Signal Requirement		N/A
6.6.3	USB 2.0		N/A

6.6.4	USB 3.2		N/A
6.6.5	USB4		N/A
6.6.6	Return Loss		N/A
6.7	Active Cables That Support Alternate Modes		N/A
6.7.1	Discover SVIDs		N/A
6.7.2	Discover Modes		N/A
6.7.3	Enter/Exit Modes		N/A
6.7.4	Power in Alternate Modes		N/A

3. Compliance of EN IEC 62680-1-2:2022

3.1 Assess result

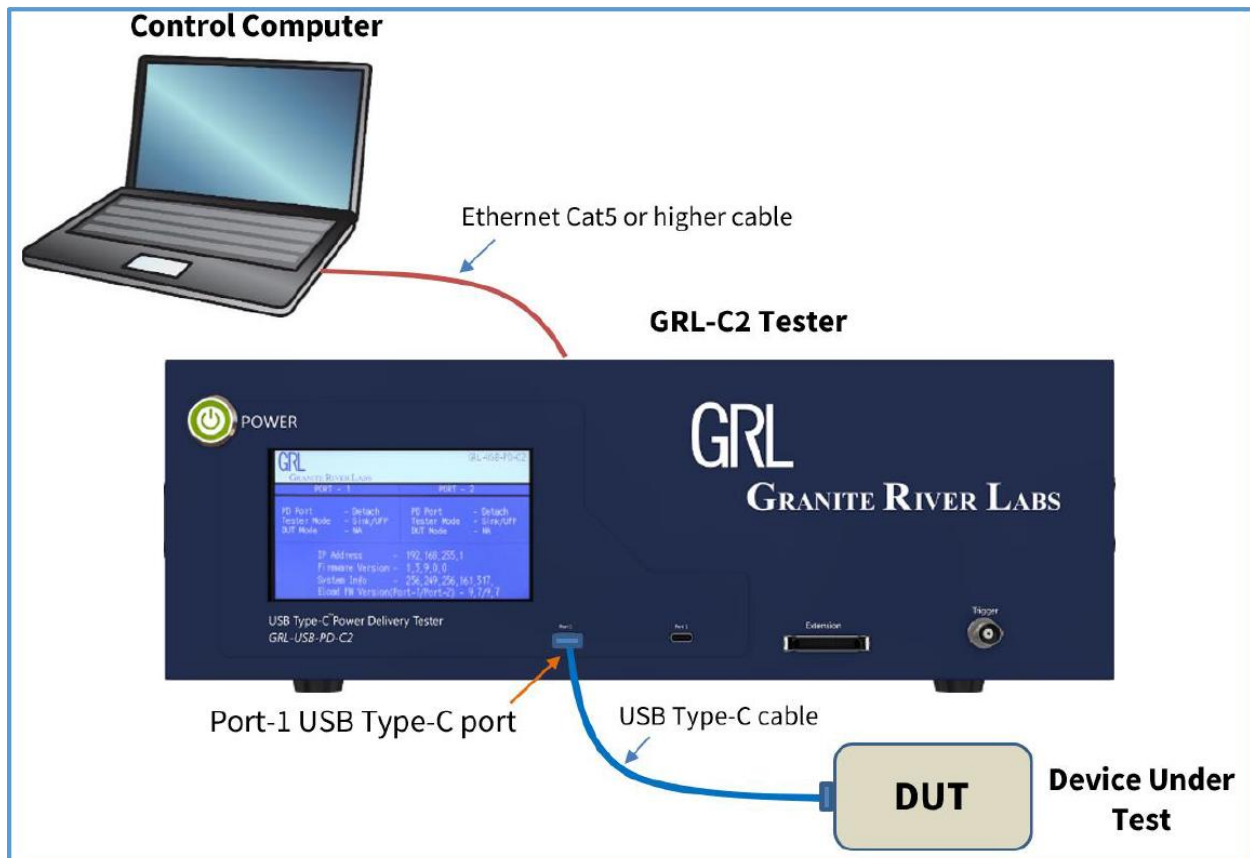
Clause	Requirement	Remark	Verdict
4	Electrical Requirements		Pass
4.1	Interoperability with other USB Specifications		Pass
4.2	Dead Battery Detection / Unpowered Port Detection		Pass
4.3	Cable IR Ground Drop (IR Drop)		N/A
4.4	Cable Type Detection		Pass
5	Cable Type Detection		Pass
6	Protocol Layer		Pass
7	Power Supply		Pass
7.1	Source Requirements		Pass
7.2	Sink Requirements		Pass
7.3	Transitions		Pass
7.4	Electrical Parameters		Pass
7.4.1	Source Electrical Parameters		Pass
7.4.2	Sink Electrical Parameters		Pass
7.4.3	Common Electrical Parameters		Pass
8	Device Policy		Pass
9	States and Status Reporting		Pass
10	Power Rules		Pass
10.1	Introduction		Pass
10.2	Source Power Rules	Only SRC PDO fixed supply 5Vdc/1A was considered in this report.	Pass
10.3	Sink Power Rules		Pass

4. Type-C Functional Test Results

4.1 Test equipment

Equipment	Manufacturer	Model No.	Serial No.	Cal Due To
USB Type-CTM Power Delivery Tester-EPR	GRL	GRL-USB-PD-C2-EPR	GRL-C2-EPR-2022115	2025/11/2

4.2 Block diagram of test setup



4.3 Limits

Compliance of EN IEC 62680-1-3:2022 clause 4

4.4 Test procedure

- (1) Vendor Information File (VIF) is provided by Vendor.
- (2) Load DUT's XML VIF File.
- (3) Select the cable that connects the primary port of the DUT to Port-1 of the GRL- C2-EPR tester hardware.
- (4) Run tests and generate test reports.

4.5 Test result

No	Test ID	Test Name	Reference spec.	Test Result
1	TD.4.11.2	TD.4.11.2 Sink Dead Battery Test	EN IEC62680-1-3 Chapter 4.5.4.8	PASS
2	TD.4.1.1	TD.4.1.1 Initial Voltage Test	EN IEC62680-1-3 Chapter 4.5	PASS
3	TD.4.2.1	TD.4.2.1 Source Connect Sink Test	EN IEC62680-1-3 Chapter 4.11	NA
4	TD.4.2.2	TD.4.2.2 Source Connect SNKAS Test	EN IEC62680-1-3 Chapter 4.5.4.9	NA
5	TD.4.2.3	TD.4.2.3 Source Connect DRP	EN IEC62680-1-3 Chapter 4.5	NA
6	TD.4.2.4	TD.4.2.4 Source Connect Try SRC DRP	EN IEC62680-1-3 Chapter 4	NA
7	TD.4.2.5	TD.4.2.5 Source Connect Try SNK DRP	EN IEC62680-1-3 Chapter 4	NA
8	TD.4.2.6	TD.4.2.6 Source Connect Audio Accessory	EN IEC62680-1-3 Chapter 4.5	NA
9	TD.4.2.7	TD.4.2.7 Source Connect DebugAccessory	EN IEC62680-1-3 Chapter 4.5	NA
10	TD.4.2.8	TD.4.2.8 Source Connect Vconn Accessory	EN IEC62680-1-3 Chapter 4.5	NA
11	TD.4.3.1	TD.4.3.1 Sink Connect Source Test	EN IEC62680-1-3 Chapter 4.5	NA
12	TD.4.3.2	TD.4.3.2 Sink Connect DRP Test	EN IEC62680-1-3 Chapter 4.5	NA
13	TD.4.3.3	TD.4.3.3 Sink Connect Try SRC DRP Test	EN IEC62680-1-3 Chapter 4	NA
14	TD.4.3.4	TD.4.3.4 Sink Connect Try SNK DRP Test	EN IEC62680-1-3 Chapter 4	NA
15	TD.4.3.5	TD.4.3.5 SinkConnect.SNKAS.Test	EN IEC62680-1-3 Chapter 4.10	NA
16	TD.4.3.6	TD.4.3.6 SinkConnect.Accessories.Test	EN IEC62680-1-3 Chapter 4	NA
17	TD.4.4.1	TD.4.4.1 SNKAS Connect Source Test	EN IEC62680-1-3 Chapter 4.5	NA
18	TD.4.4.2	TD.4.4.2 SNKAS Connect DRP Test	EN IEC62680-1-3 Chapter 4.5	NA
19	TD.4.4.3	TD.4.4.3SNKAS Connect Try SRC DRP Test	EN IEC62680-1-3 Chapter 4.5	NA
20	TD.4.4.4	TD.4.4.4 SNKAS Connect Try SNK DRP Test	EN IEC62680-1-3 Chapter 4.5	NA
21	TD.4.4.5	TD.4.4.5 SNKAS Connect SNKAS Test	EN IEC62680-1-3 Chapter 4.5	NA
22	TD.4.4.6	TD.4.4.6 SNKAS Connect Audio Acc	EN IEC62680-1-3 Chapter 4.5	NA
23	TD.4.4.7	TD.4.4.7 SNKAS Connect Debug Accessory	EN IEC62680-1-3 Chapter 4.5	NA
24	TD.4.4.8	TD.4.4.8 SNKAS Connect PoweredAcc	EN IEC62680-1-3 Chapter 4.5	NA
25	TD.4.5.1	TD.4.5.1 DRP Connect Sink Test	EN IEC62680-1-3 Chapter 4.5.4.8	NA
26	TD.4.5.2	TD.4.5.2 DRP Connect SNKAS Test	EN IEC62680-1-3 Chapter 4.6	NA

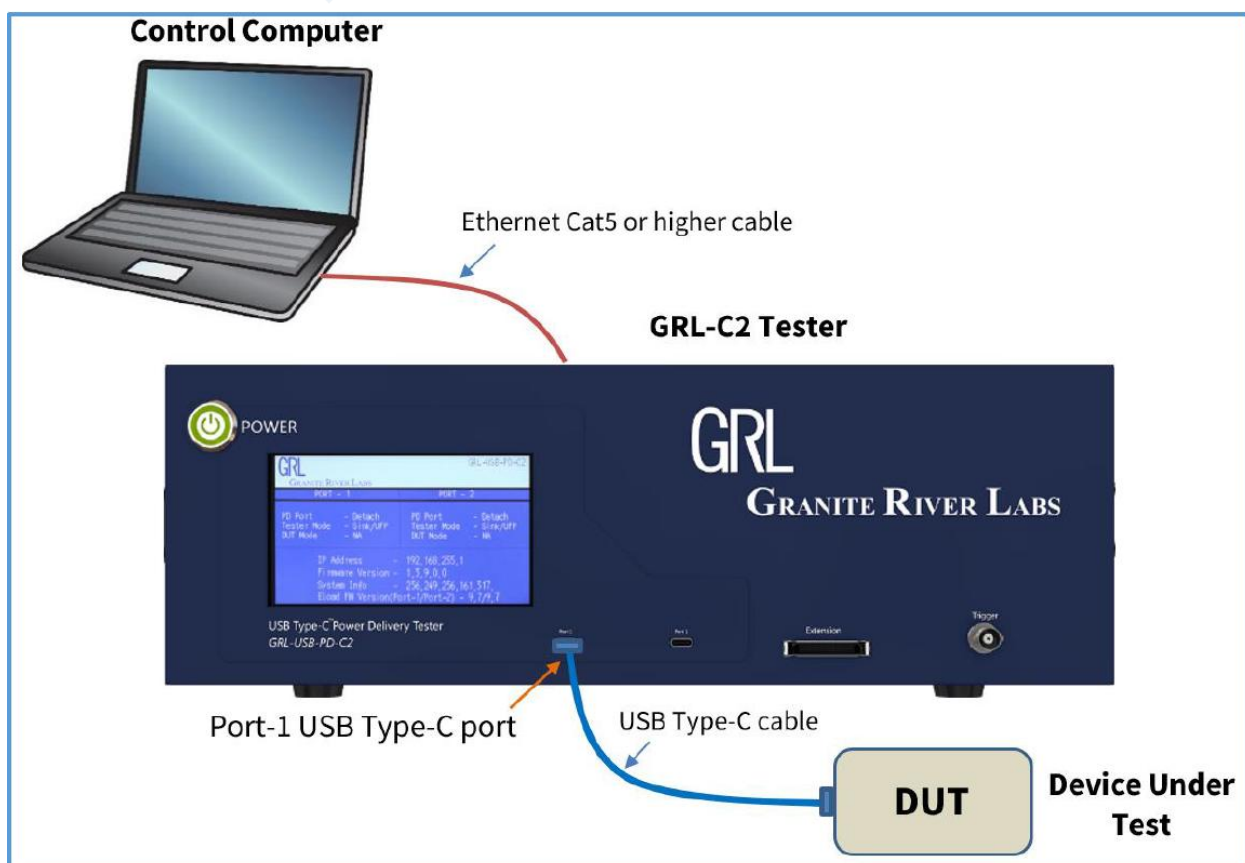
No	Test ID	Test Name	Reference spec.	Test Result
27	TD.4.5.3	TD.4.5.3 DRP Connect Source Test	EN IEC62680-1-3 Chapter 4.5,4.8	NA
28	TD.4.5.4	TD.4.5.4 DRP Connect DRP Test	EN IEC62680-1-3 Chapter 4.5,4.8	NA
29	TD.4.5.5	TD.4.5.5 DRP Connect Try SRC DRP Test	EN IEC62680-1-3 Chapter 4.5	NA
30	TD.4.5.6	TD.4.5.6 DRP Connect Try SNK DRP Test	EN IEC62680-1-3 Chapter 4	NA
31	TD.4.6.1	TD.4.6.1 Try SRC DRP Connect Source Test	ENIEC62680-1-3 Chapter 4	NA
32	TD.4.6.2	TD.4.6.2 Try SRC DRP Connect DRP Test	EN IEC62680-1-3 Chapter 4.5	NA
33	TD.4.6.3	TD.4.6.3 Try SRC DRP Connect Try SRC DRP Test	ENIEC62680-1-3 Chapter 4	NA
34	TD.4.6.4	TD.4.6.4 Try SRC DRP Connect Try SNK DRP Test	EN IEC62680-1-3 Chapter 4	NA
35	TD.4.6.5	TD.4.6.5 Try SRC DRP Connect Sink Test	EN IEC62680-1-3 Chapter 4.4,4.5,4.6	NA
36	TD.4.6.6	TD.4.6.6 Try SRC DRP Connect SNKAS Test	EN IEC62680-1-3 Chapter 4	NA
37	TD.4.7.1	TD.4.7.1 Try SNK DRP Connect Source Test	EN IEC62680-1-3 Chapter 4.5	PASS
38	TD.4.7.2	TD.4.7.2 Try SNK DRP Connect DRP Test	EN IEC62680-1-3 Chapter 4.5	PASS
39	TD.4.7.3	TD.4.7.3 Try SNK DRP Connect Try SRC DRP Test	ENIEC62680-1-3 Chapter 4.5	PASS
40	TD.4.7.4	TD.4.7.4 Try SNK DRP Connect Try SNK DRP Test	EN IEC62680-1-3 Chapter 4	PASS
41	TD.4.7.5	TD.4.7.5 Try SNK DRP Connect Sink Test	ENIEC62680-1-3 Chapter 4	PASS
42	TD.4.7.6	TD.4.7.6 Try SNK DRP Connect SNKAS Test	EN IEC62680-1-3 Chapter 4	PASS
43	TD.4.8.1	TD.4.8.1 DRP Connect Audio Acc Test	EN IEC62680-1-3 Chapter 4.5	PASS
44	TD.4.8.2	TD.4.8.2 DRP Connect Debug Acc Test	EN IEC62680-1-3 Chapter 4.5	PASS
45	TD.4.8.3	TD.4.8.3 DRP Connect Vconn Accessory Test	ENIEC62680-1-3 Chapter 4.5	PASS
46	TD.4.9.1	TD.4.9.1 Source Suspend Test	EN IEC62680-1-3 Chapter 4.6	NA
47	TD.4.9.2	TD.4.9.2 USB Type C Current Advertisement Test	EN IEC62680-1-3 Chapter 4.6,4.7,4.8	PASS
48	TD.4.9.3	TD.4.9.3 Source PR Swap Test	EN IEC62680-1-3 Chapter 4.5	PASS
49	TD.4.9.4	TD.4.9.4 Source Vconn Swap Test	EN IEC62680-1-3 Chapter 4	NA
50	TD.4.9.5	TD.4.9.5 Source Alternate Mode Test	EN IEC62680-1-3 Chapter 4	NA
51	TD.4.10.1	TD.4.10.1 Sink Power Sub States Test	EN IEC62680-1-3 Chapter 4.4,4.5,4.6	PASS
52	TD.4.10.2	TD.4.10.2 Sink Power Precedence Test	EN IEC62680-1-3 Chapter 4	PASS
53	TD.4.10.3	TD.4.10.3 Sink Suspend Test	EN IEC62680-1-3 Chapter 4.5	PASS
54	TD.4.10.4	TD.4.10.4 Sink PR Swap Test	EN IEC62680-1-3 Chapter 4.5	PASS
55	TD.4.10.5	TD.4.10.5 Sink.VCONN Swap Test	EN IEC62680-1-3 Chapter 4.5	NA
56	TD.4.10.6	TD.4.10.6 Sink Alternate Mode Test	EN IEC62680-1-3 Chapter 4	NA
57	TD.4.11.1	TD.4.11.1 DR Swap Test	EN IEC62680-1-3 Chapter 4	PASS
58	TD.4.12.2	TD.4.12.2 Hub Port Types Test	EN IEC62680-1-3 Chapter 4	NA
59	TD.4.1.2	TD.4.1.2 Unpowered CC Voltage Test	EN IEC62680-1-3 Chapter 4.5	NA
60	TD.4.13.5	TD.4.13.5 Cable EnterUSB and Data Reset Test	EN IEC62680-1-3 Chapter 5.4	NA

5. PD Test Results

5.1 Test equipment

Equipment	Manufacturer	Model No.	Serial No.	Cal Due To
USB Type-CTM Power Delivery Tester-EPR	GRL	GRL-USB-PD-C2-EPR	GRL-C2-EPR-2022115	2025/11/2

5.2 Block diagram of test setup



5.3 Limits

Compliance of EN IEC 62680-1-2:2022

5.4 Test procedure

- (1) Vendor Information File (VIF) is provided by Vendor.
- (2) Load DUT's XML VIF File.
- (3) Select the cable that connects the primary port of the DUT to Port-1 of the GRL- C2-EPR tester hardware.
- (4) Run tests and generate test reports.

5.5 Test result

No	Test ID	Test Name	Test Result
1	TEST.PD.PHY.ALL.1	TEST.PD.PHY.ALL.1 Transmit Bit Rate and the Drift	PASS
2	TEST.PD.PHY.ALL.2	TEST.PD.PHY.ALL.2 Transmitter Eye Diagram	PASS
3	TEST.PD.PHY.ALL.3	TEST.PD.PHY.ALL.3 Collision Avoidance	PASS
4	TEST.PD.PHY.ALL.4	TEST.PD.PHY.ALL.4 Bus Idle Detection	PASS
5	TEST.PD.PHY.ALL.5	TEST.PD.PHY.ALL.5 Receiver Interference Rejection	PASS
6	TEST.PD.PHY.ALL.6	TEST.PD.PHY.ALL.6 Invalid SOP	PASS
7	TEST.PD.PHY.ALL.7	TEST.PD.PHY.ALL.7 Valid SOP*	PASS
8	TEST.PD.PHY.ALL.8	TEST.PD.PHY.ALL.8 Incorrect CRC	PASS
9	TEST.PD.PHY.ALL.9	TEST.PD.PHY.ALL.9 Receiver Input Impedance	PASS
10	TEST.PD.PHY.PORT.1	TEST.PD.PHY.PORT.1 Invalid Reset Signals	PASS
11	TEST.PD.PROT.ALL.1	TEST.PD.PROT.ALL.1 Corrupted GoodCRC	PASS
12	TEST.PD.PROT.ALL.2	TEST.PD.PROT.ALL.2 Soft Reset and Hard Reset	PASS
13	TEST.PD.PROT.ALL.3	TEST.PD.PROT.ALL.3 Soft Reset response	PASS
14	TEST.PD.PROT.ALL.4	TEST.PD.PROT.ALL.4 Reset Signals and MessageID	PASS
15	TEST.PD.PROT.ALL.5	TEST.PD.PROT.ALL.5 Unrecognized Message	PASS
16	TEST.PD.PROT.ALL3.1	TEST.PD.PROT.ALL3.1 Get_Status Response	PASS
17	TEST.PD.PROT.ALL3.2	TEST.PD.PROT.ALL3.2 Get_Manufacturer_Info Response	PASS
18	TEST.PD.PROT.ALL3.3	TEST.PD.PROT.ALL3.3 Invalid Manufacturer Info Target	PASS
19	TEST.PD.PROT.ALL3.4	TEST.PD.PROT.ALL3.4 Invalid Manufacturer Info Ref	PASS
20	TEST.PD.PROT.ALL3.5	TEST.PD.PROT.ALL3.5 Chunked Extended Message Response	PASS
21	TEST.PD.PROT.ALL3.6	TEST.PD.PROT.ALL3.6 ChunkSenderResponseTimer Timeout	PASS
22	TEST.PD.PROT.ALL3.7	TEST.PD.PROT.ALL3.7 Security Messages Supported	PASS
23	TEST.PD.PROT.ALL3.8	TEST.PD.PROT.ALL3.8 Get Revision Response	PASS
24	TEST.PD.PROT.PORT3.1	TEST.PD.PROT.PORT3.1 Get Battery Status Response	PASS
25	TEST.PD.PROT.PORT3.2	TEST.PD.PROT.PORT3.2 Invalid Battery Status	PASS
26	TEST.PD.PROT.PORT3.3	TEST.PD.PROT.PORT3.3 Get Battery Cap Response	PASS
27	TEST.PD.PROT.PORT3.4	TEST.PD.PROT.PORT3.4 Invalid Battery Capabilities Reference	PASS

28	TEST.PD.PROT.PORT3.5	TEST.PD.PROT.PORT3.5 Get Country Codes Response	PASS
29	TEST.PD.PROT.PORT3.6	TEST.PD.PROT.PORT3.6 Get Country Info Response	PASS
30	TEST.PD.PROT.PORT3.7	TEST.PD.PROT.PORT3.7 Unchunked Extended Message Supported	NA
31	TEST.PD.PROT.SRC.1	TEST.PD.PROT.SRC.1 Get_Source_Cap Response	PASS
32	TEST.PD.PROT.SRC.2	TEST.PD.PROT.SRC.2 Get_Source_Cap No Request	PASS

33	TEST.PD.PROT.SRC.3	TEST.PD.PROT.SRC.3 Sender Response Timer Deadline	PASS
34	TEST.PD.PROT.SRC.4	TEST.PD.PROT.SRC.4 Reject Request	PASS
35	TEST.PD.PROT.SRC.5	TEST.PD.PROTSRC.5 Reject Request Invalid Object Position	PASS
36	TEST.PD.PROTSRC.6	TEST.PD.PROT.SRC.6 Atomic Message Sequence -Request	PASS
37	TEST.PD.PROT.SRC.7	TEST.PD.PROT.SRC.7 DR_Swap	PASS
38	TEST.PD.PROT.SRC.8	TEST.PD.PROTSRC.8VCONN Swap Response	PASS
39	TEST.PD.PROT.SRC.9	TEST.PD.PROT.SRC.9 PR_Swap Response	PASS
40	TEST.PD.PROTSRC.10	TESTPD.PROT.SRC.10PR_Swap-PSSourceOnTimer Timeout	PASS
41	TEST.PD.PROT.SRC.11	TEST.PD.PROT.SRC.11 Unexpected Message Received in Ready State	PASS
42	TEST.PD.PROT.SRC.12	TESTPD.PROTSRC.12 GetSinkCap Response	PASS
43	TEST.PD.PROT.SRC.13	TEST.PD.PROTSRC.13PR Swap GoodCRC not sent in Response to PS_RDY	PASS
44	TEST.PD.PROT.SRC3.1	TEST.PD.PROT.SRC3.1 SourceCapabilityTimer Timeout	PASS
45	TEST PDPROTSRC3.2	TEST.PD.PROTSRC3.2 SenderResponseTimer Timeout	PASS
46	TESTPD.PROTSRC3.3	TESTPD.PROTSRC3.3 Get Source Cap Extended Response	PASS
47	TEST.PD.PROT.SRC3.4	TEST.PD.PROT.SRC3.4 Alert Response Source Input Change	PASS
48	TEST.PD.PROT.SRC3.5	TEST.PD.PROT.SRC3.5 Alert Response Battery Status Change	PASS
49	TEST.PD.PROT.SRC3.6	TEST.PD.PROT.SRC3.6 Soft Reset Sent when SinkTxOK	PASS
50	TEST.PD.PROTSRC3.7	TEST.PD.PROT.SRC3.7 Get_PPS_Status Response	NA
51	TEST.PD.PROT.SRC3.8	TESTPD.PROT.SRC3.8 SourcePPSCommTimer Deadline	NA
52	TEST.PDPROT.SRC3.9	TEST.PD.PROT.SRC3.9 SourcePPSCommTimer Timeout	NA
53	TEST.PD.PROT.SRC3.10	TEST.PD.PROT.SRC3.10 SourcePPSCommTimer Stopped	NA
54	TEST.PD.PROT.SRC3.11	TEST.PD.PROTSRC3.11GoodCRC Specification Revision Compatibility	PASS
55	TEST.PD.PROT.SRC3.12	TESTPD.PROTSRC3.12 FR Swap Without Signaling	PASS
56	TEST.PD.PROT.SRC3.13	TEST.PD.PROT.SRC3.13 Cable Type Detection	PASS
57	TEST.PD.PROT.SRC3.14	TEST.PD.PROTSRC3.14 Source Info	PASS
58	TEST.PD.PROT.SRC3.15	TEST.PD.PROT.SRC3.15 Alert Response Extended Alert	PASS
59	TEST.PD.PROT.SNK.1	TEST.PD.PROT.SNK.1 Get_Sink_Cap Response	PASS
60	TESTPD.PROTSNK.2	TEST.PD.PROT.SNK.2 Get_Source_Cap Response	PASS
61	TEST.PD.PROT.SNK.3	TEST.PD.PROTSNK.3 SinkWaitCapTimer Deadline	PASS

62	TEST.PD.PROT.SNK.4	TEST.PD.PROTSNK.4 SinkWaitCapTimer Timeout	PASS
63	TEST.PD.PROT.SNK.5	TEST.PD.PROTSNK.5 SenderResponseTimer Deadline	PASS
64	TEST.PD.PROT.SNK.6	TEST.PD.PROT.SNK.6 SenderResponseTimer Timeout	PASS
65	TEST.PD.PROT.SNK7	TEST.PD.PROTSNK7PSTransitionTimer Timeout	PASS
66	TEST.PD.PROT.SNK.8	TEST.PD.PROT.SNK.8 Atomic Message Sequence -Accept	PASS
67	TEST.PD.PROT.SNK.9	TEST.PD.PROT.SNK.9 Atomic Message Sequence-PS RDY	PASS
68	TEST.PD.PROT.SNK.10	TEST.PD.PROT.SNK.10 DR_Swap Request	PASS
69	TESTPD.PROTSNK.11	TEST.PD.PROT.SNK.11VCONN_Swap Request	PASS
70	TESTPD.PROT.SNK.12	TEST.PD.PROT.SNK.12 PR_Swap PSSourceOffTimer Timeout	PASS
71	TEST.PD.PROTSNK.13	TEST.PD.PROT.SNK.13PR_Swap-Request SenderResponseTimer Timeout	PASS
72	TEST.PD.PROTSNK.14	TEST.PD.PROT.SNK.14 Valid Use of GoodCRC on Power up	PASS
73	TEST.PD.PROT.SNK3.1	TEST.PD.PROT.SNK3.1 Get_Source_Cap_Extended	PASS
74	TESTPD.PROT.SNK3.2	TEST.PDPROT.SNK3.2 Alert Response Source Input Change	PASS
75	TESTPD.PROTSNK3.3	TEST.PD.PROT.SNK3.3 Alert Response Battery Status Change	PASS
76	TEST.PD.PROT.SNK3.4	TEST.PD.PROT.SNK3.4 Soft_Reset Sent Regardless of Rp Value	PASS
77	TESTPD.PROT.SNK3.5	TEST.PD.PROT.SNK3.5 Sink PPS Normal Operation	PASS
78	TEST.PD.PROT.SNK3.6	TEST.PD.PROT.SNK3.6 Revision Number Test	PASS
79	TEST.PD.PROT.SNK3.7	TEST.PD.PROT.SNK3.7 GoodCRC Specification Revision Compatibility	PASS
80	TEST.PD.PROT.SNK3.9	TEST.PD.PROT.SNK3.9 Alert Response Extended Alert	PASS
81	TEST.PD.VDM.SNK1	TEST.PD.VDM.SNK.1 Discovery Process and Enter Mode	PASS
82	TEST.PD.VDM.SNK.2	TEST.PD.VDM.SNK.2 Exit Mode without Entering	PASS
83	TEST.PD.VDM.SNK.5	TEST.PD.VDM.SNK.5DR Swap in Modal Operation	PASS
84	TEST.PD.VDM.SNK.6	TEST.PD.VDM.SNK.6 Structured VDM Revision Number Test	PASS
85	TEST.PD.VDM.SNK.7	TEST.PD.VDM.SNK.7 Unrecognized VID in Unstructured VDM	PASS
86	TEST.PD.VDM.CBL.1	TEST.PD.VDM.CBL.1 Discovery Process and Enter Mode	NA
87	TEST.PD.VDM.SRC.1	TEST.PD.VDM.SRC.1 Discovery Process and Enter Mode	PASS
88	TEST.PD.VDM.SRC.2	TEST.PD.VDM.SRC.2 Invalid Fields-Discover Identity	PASS
89	TESTPD.VDM.CBL3.1	TEST.PD.VDM.CBL3.1 Revision Number Test	NA
90	TEST.PD.PS.SRC.1	TEST.PD.PS.SRC.1 Multiple Request Messages	PASS
91	TEST.PD.PS.SRC.2	TEST.PDPS.SRC.2 PDO Transition	PASS

92	TEST.PD.PS.SRC.3	TEST.PD.PS.SRC.3Initial Source PDO Transition Post PR Swap	PASS
93	TEST.PD.PS.SRC.4	TEST.PD.PS.SRC.4 Source Behavior with Capability Mismatch Bit	PASS
94	TEST.PD.PS.SRC.5	TEST.PD.PS.SRC.5 Source Hard Reset Test	PASS
95	TEST.PD.PS.SNK.1	TEST.PD.PS.SNK.1 PDO Transition	PASS
96	TEST.PD.PS.SNK.2	TEST.PD.PS.SNK.2 Initial Sink PDO Transition	PASS
97	TEST.PD.PSSNK.3	TEST.PD.PS.SNK.3 Multiple Request Load Test Post PR Swap	PASS
98	TEST.PD.EPR.SRC3.1	TEST.PD.EPR.SRC3.1 EPR Entry Process-UUT as VCONN Source	PASS
99	TEST.PD.EPR.SRC3.2	TEST.PD.EPR.SRC3.2 EPR Entry Process-Tester as VCONN Source	NA
100	TEST.PD.EPR.SRC3.3	TEST.PD.EPR.SRC3.3EPR Entry failed-EPR Mode Capable bit not set in RDO	NA
101	TESTPD.EPR.SRC3.4	TEST.PD.EPR.SRC3.4 EPR Entry failed-Tester as VCONN source	NA
102	TEST.PD.EPR.SRC3.5	TEST.PD.EPR.SRC3.5 EPR Entry Failed EPR_Mode(Reserved) message	NA
103	TEST.PD.EPR.SRC3.6	TEST.PD.EPR.SRC3.6 EPR Entry Failed-Cable not EPR capable	NA
104	TEST.PD.EPR.SRC3.7	TEST.PD.EPR.SRC3.7 EPR Entry Failed Interrupted by EPR_Get_Sink_Cap message	NA
105	TEST.PD.EPR.SRC3.8	TEST.PD.EPR.SRC3.8 EPR mode-Request message response	NA
106	TEST.PD.EPR.SRC3.9	TEST.PD.EPR.SRC3.9 EPR mode- EPR Get Source Cap message	NA
107	TEST.PD.EPR.SRC3.10	TEST.PD.EPR.SRC3.10 SPR mode EPR_Get Source_Cap message	PASS
108	TESTPD.EPR.SRC3.11	TEST.PD.EPR.SRC3.11 EPR Mode Exit by EPR Mode Exit message	NA
109	TEST.PD.EPRSRC3.12	TEST.PD.EPRSRC3.12 EPR mode-Get_Source_Cap message and Request message response	NA
110	TEST.PD.EPRSRC3.13	TEST.PD.EPR.SRC3.13 EPR mode-tSourceEPRKeepAlive Timeout	NA
111	TEST.PD.EPR.SRC3.14	TEST.PD.EPR.SRC3.14 EPR mode-EPR Request with Incorrect copy of PDO	NA
112	TEST.PD.EPR.SRC3.15	TEST.PD.EPR.SRC3.15 DiscoverIdentityCounter and DiscoverIdentityTimer check for SOP1	NA
113	TEST.PD.EPR.SRC3.16	TEST.PD.EPR.SRC3.16 PR_Swap for the UUT as EPR Source	NA
114	TEST.PD.EPR.SNK3.1	TEST.PD.EPR.SNK3.1 EPR Entry Process Success	PASS
115	TESTPD.EPRSNK3.2	TEST.PD.EPR.SNK3.2 EPR Entry Fail tEnterEPR Timer Timeout	NA
116	TEST.PD.EPR.SNK3.3	TEST.PD.EPR.SNK3.3 EPR Fail by EPR Enter Failed Message	NA
117	TEST.PD.EPR.SNK3.4	TEST.PD.EPR.SNK3.4 EPR Entry Fail tFirstSourceCap Timer Timeout	NA
118	TESTPD.EPR.SNK3.5	TEST.PD.EPR.SNK3.5 EPR Exit by Incorrect EPR Source Cap	NA
119	TEST.PD.EPR.SNK3.6	TEST.PD.EPR.SNK3.6 EPR Exit by EPR Exit Message	NA
120	TESTPD.EPR.SNK3.8	TEST.PD.EPR.SNK3.8 EPR Exit by Source Cap Message	NA
121	TEST.PD.EPR.SNK3.9	TEST.PD.EPR.SNK3.9 EPR Entry failed due to SourceCap	NA

122	TESTPD.EPR.SNK3.10	TEST.PD.EPR.SNK3.10 EPR Exit fail due to SinkWaitCapTimer timeout	NA
123	TEST.PD.EPR.SNK3.11	TEST.PD.EPR.SNK3.11 PR_Swap for the UUT as the EPR Sink	NA
124	TESTPD.PS.EPR.SRC3.1	TEST.PD.PS.EPR.SRC3.1 Multiple EPR Request Load Test	NA
125	TEST.PD.PS.EPR.SRC3.2	TEST.PD.PS.EPR.SRC3.2 PDO Transitions in EPR Mode	NA
126	TEST.PD.FRS.SRC3.1	TEST.PD.FRS.SRC3.1 Normal Conditions	NA
127	TEST.PD.FRS.SRC3.2	TEST.PD.FRS.SRC3.2 Provider Only Checks	NA
128	TESTPDFRSSRC3.3	TEST.PD.FRS.SRC3.3 GoodCRC Not Sent In Response To Accept	NA
129	TESTPD.FRS.SRC3.4	TEST.PD.FRS.SRC3.4 GoodCRC Not Sent In Response To PS_RDY	NA
130	TEST.PD.FRSSRC3.5	TESTPD.FRS.SRC3.5PSSourceOnTimer Deadline	NA
131	TEST.PD.FRSSRC3.6	TEST.PD.FRS.SRC3.6PSSourceOnTimer Timeout	NA
132	TESTPD.FRS.SNK3.1	TEST.PD.FRS.SNK3.1 Normal Conditions	PASS
133	TEST.PD.FRS.SNK3.2	TEST.PD.FRS.SNK3.2 Normal Conditions Consumer Only	NA
134	TEST.PD.FRS.SNK3.3	TEST.PD.FRS.SNK3.3 FR_Swap Not Sent	NA
135	TEST.PD.FRS.SNK3.4	TEST.PD.FRS.SNK3.4 SendResponseTimer Timeout	PASS
136	TESTPD.FRS.SNK3.5	TESTPD.FRS.SNK3.5 PSSourceOffTimer Deadline	PASS
137	TEST.PD.FRS.SNK3.6	TEST.PD.FRS.SNK3.6PSSourceOffTimer Timeout	PASS
138	TESTPDFRS.SNK3.7	TEST.PD.FRS.SNK3.7 GoodCRC Not Sent in Response to PS_RDY	PASS
139	TEST.PD.USB4.DRST.1	TEST.PD.USB4.DRST.1-Data Reset command response of UFP UUT	PASS
140	TESTPD.USB4DRST.2	TEST.PD.USB4.DRST.2-Data_Reset command response of UFP UUT, Invalid Sequence	NA
141	TEST.PD.USB4.DRST.3	TEST.PD.USB4.DRST.3-Data_Reset command response of UFP UUT Sourcing Vconn	NA
142	TEST.PD.USB4.DRST.4	TEST.PD.USB4.DRST.4-DataReset command response of UFP UUT Sourcing Vconn - Invalid Sequence	NA
143	TEST.PD.USB4.DRST.5	TEST.PD.USB4.DRST.5-Data Reset command response of DFP UUT Sourcing Vconn	NA
144	TEST.PD.USB4.DRST.6	TEST.PD.USB4.DRST.6-Data_Reset command response of DFP UUT,UFP Sourcing Vconn	NA
145	TEST.PD.USB4.DRST.7	TEST.PD.USB4.DRST.7-Data reset command response of DFP UUT,UFP Sourcing Vconn VCONNDischargeTimer expiry check	NA
146	TEST.PD.USB4.EUSB.1	TEST.PD.USB4.EUSB.1-Enter_USB Message response of UFP UUT-Valid Mode	NA
147	TEST.PD.USB4.EUSB.2	TEST.PD.USB4.EUSB.2-Enter_USB Message response of UFP UUT-Invalid Mode	NA
148	TEST.PD.USB4.EUSB.3	TEST.PD.USB4.EUSB.3-Enter USB Flow-USB4 DFP Connected to USB4 UFP using an Active Cable	NA
149	TEST.PD.USB4.EUSB.4	TESTPD.USB4.EUSB.4-DR_Swap after Entering USB4 Mode entry	NA

150	TEST.PD.USB4.EUSB.5	TESTPD.USB4.EUSB.5-tEnterUSBWait check for USB4 DFP	NA
151	TEST.PD.USB4.CBL.1	TEST.PD.USB4.CBL.1-Enter_USB Message response of cable UUT-Valid Mode	NA
152	TEST.PD.USB4.CBL.2	TEST.PD.USB4.CBL.2-Enter_USB Message response of Cable UUT-Invalid Mode	NA
153	2.1	Common Checks	PASS
154	2.2	Common Procedures	PASS

6. Photos of the EUT

Photo 1

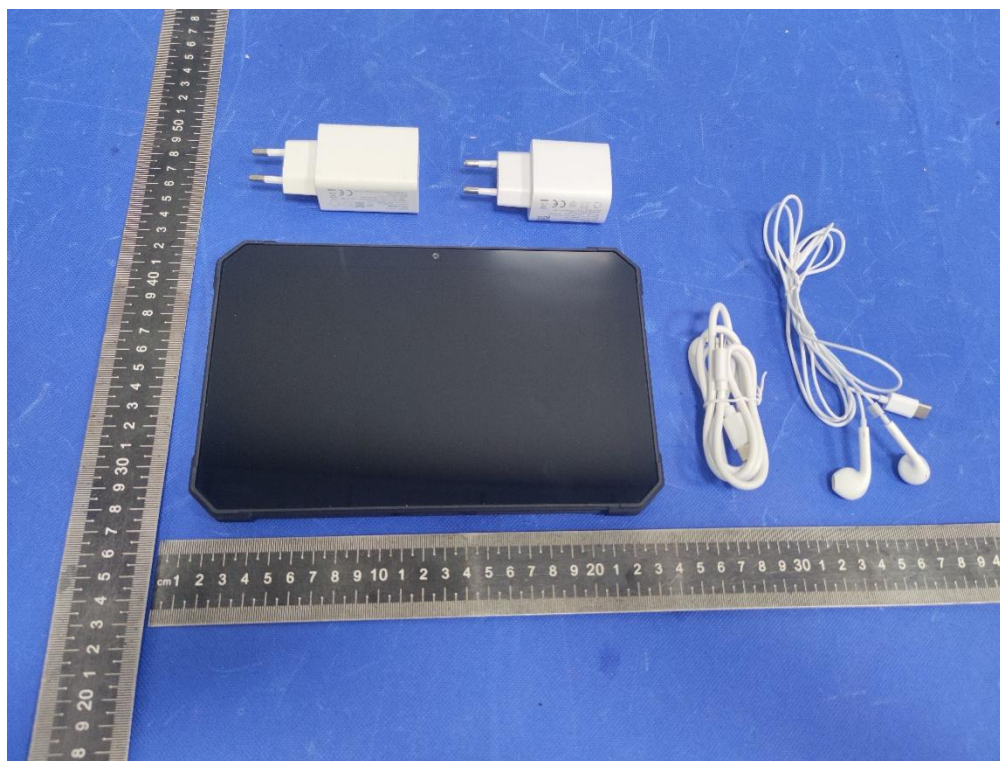


Photo 2

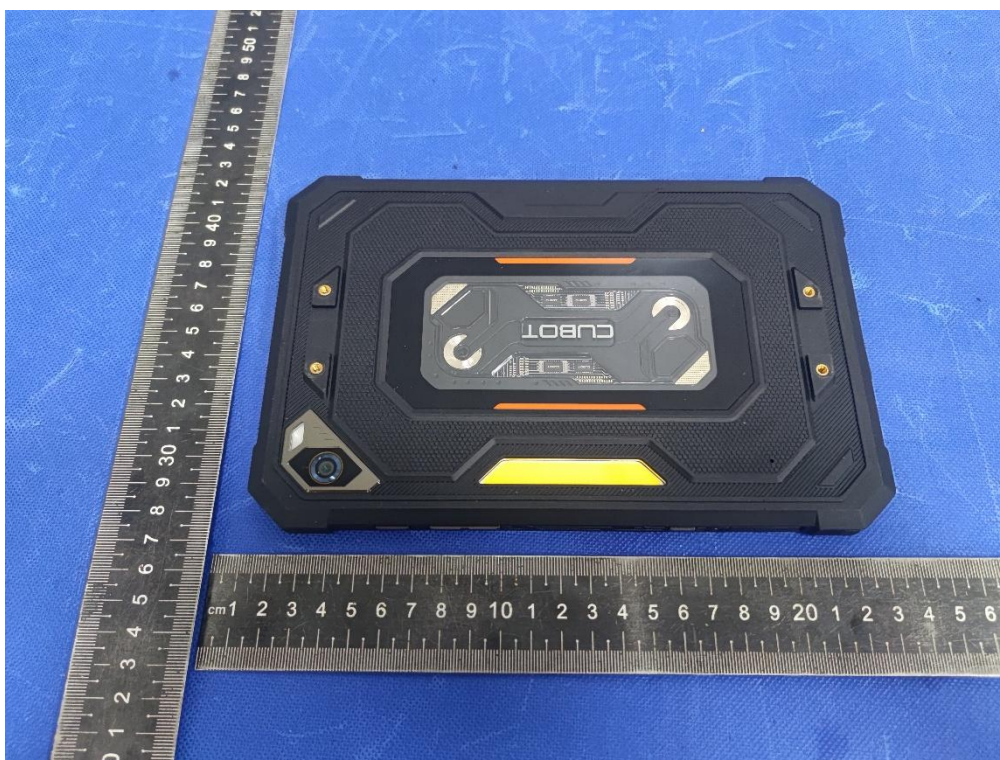


Photo 3

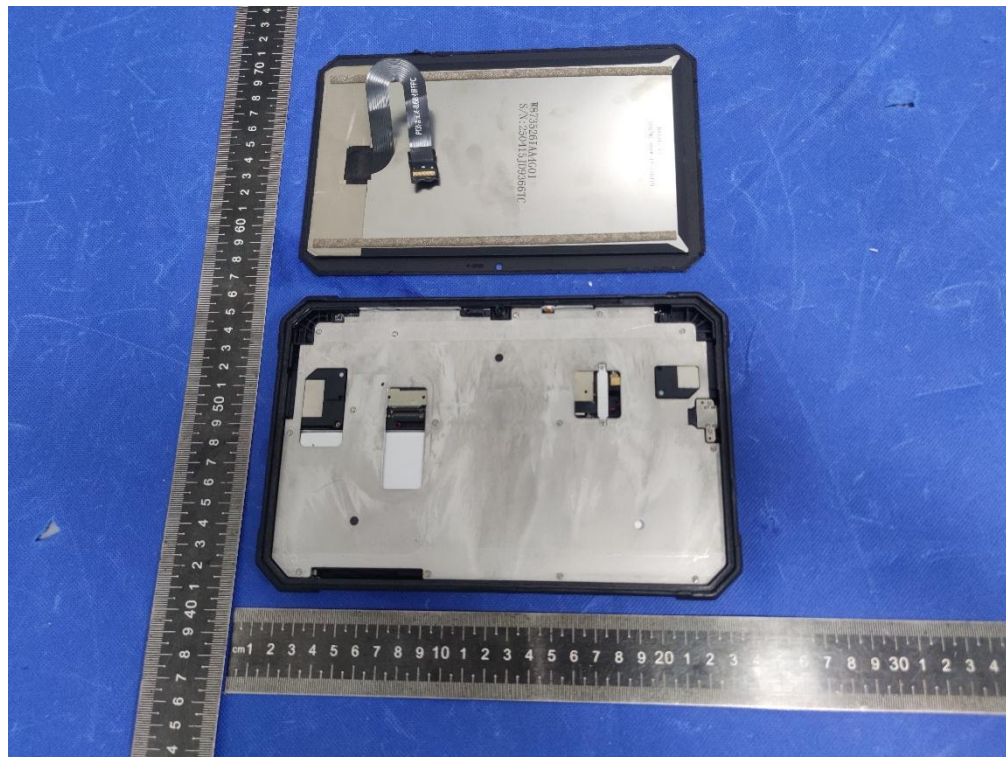


Photo 4



Photo 5

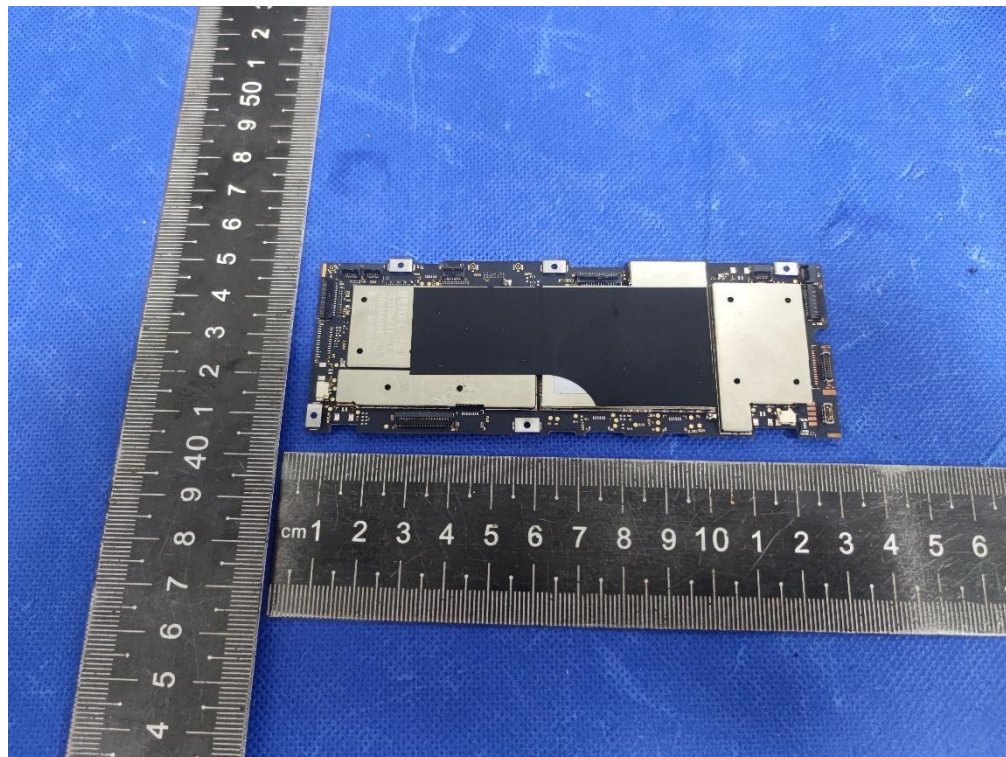


Photo 6

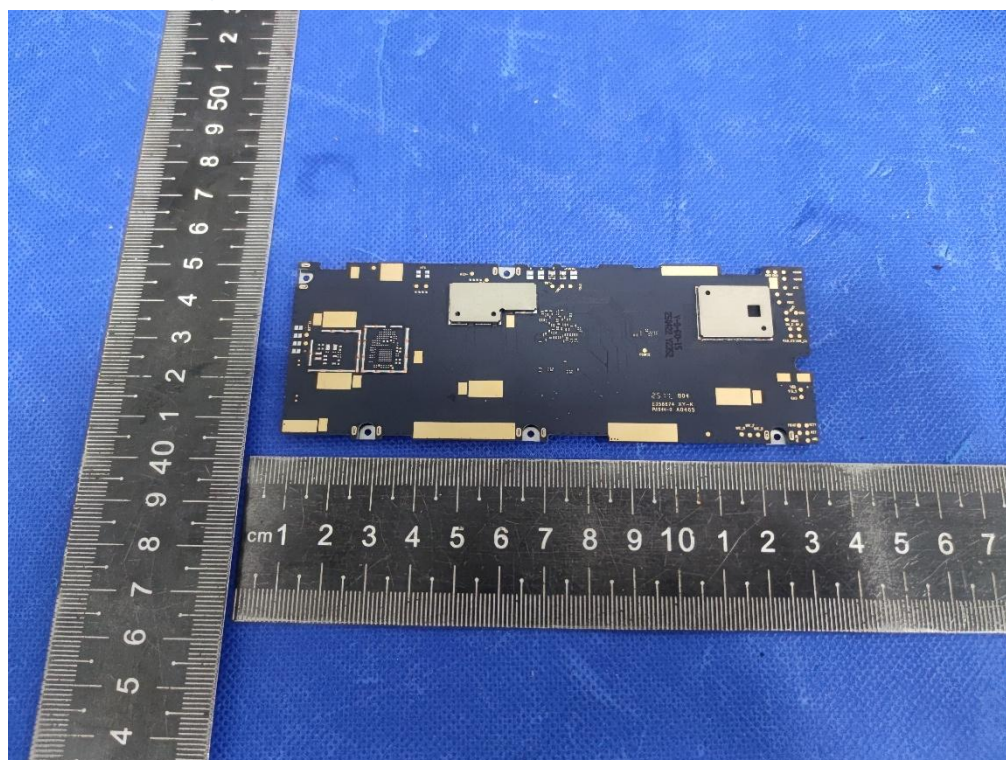


Photo 7

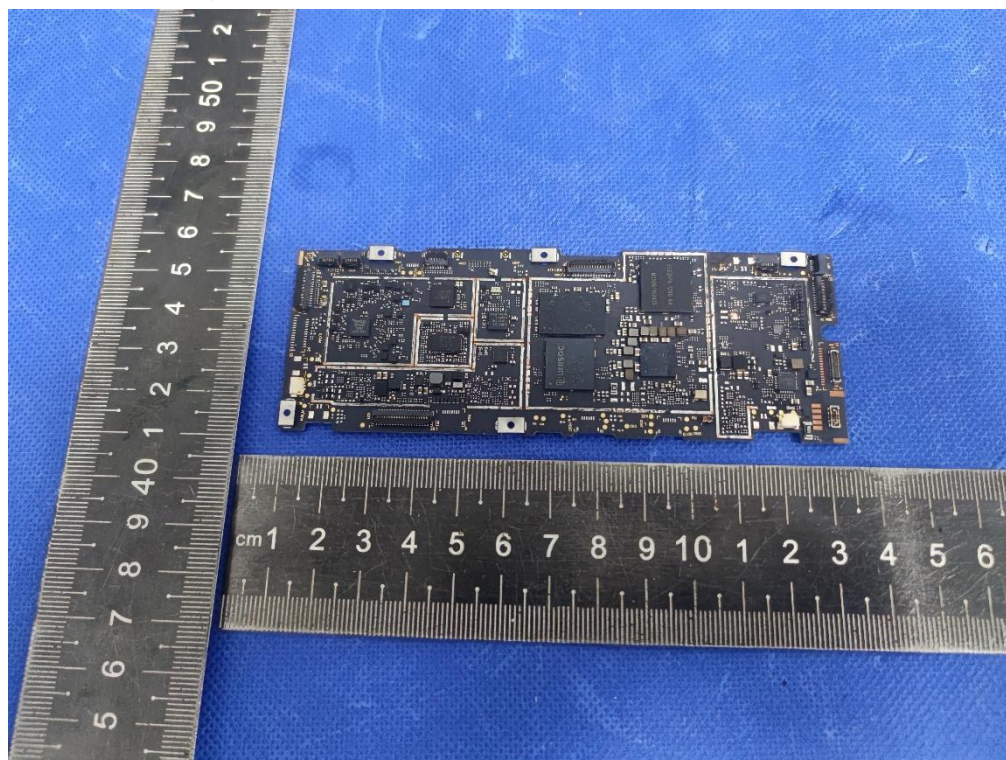
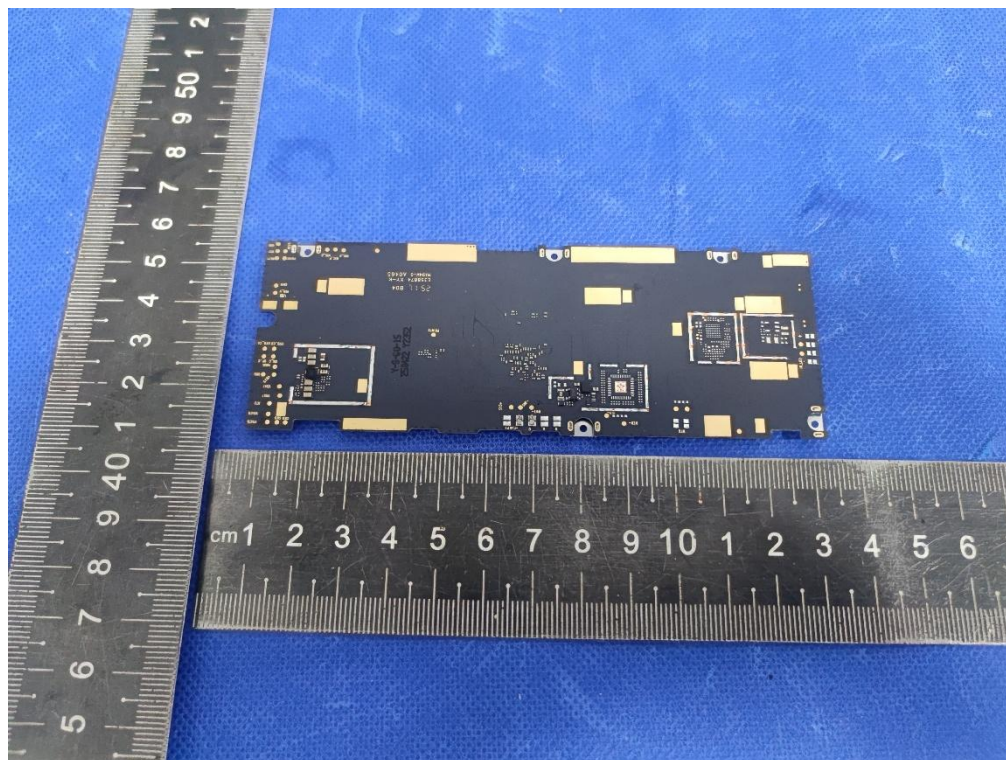


Photo 8



END OF REPORT